

**Notice of Allowability**

Application No.

09/989,027

Examiner

Prabodh M. Dharia

Applicant(s)

YAMASAKI, NOBUO

Art Unit

2629

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 02-22-2006.
2. ☒ The allowed claim(s) is/are 1,3-10 and 12-27 renumbered as 1-25.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

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1. **Status:** Please all the replies and correspondence should be addressed to Examiner's new art unit 2629. Receipt is acknowledged of papers submitted 02-22-2006 under amendments and request for reconsideration, which, have been placed of record in the file. Claims 1,3-10 and 12-27, are pending in this office action. Claims 2 and 11 are cancelled.

***Response to Amendment***

2. Applicant has amended claims 1,10. Applicant's arguments, see Remark, filed 02-22-06, with respect to Claims 1 and 10 regarding "a memory device driver for controlling the write of the graphic data held in the memory devices into the pixels in order to perform a second display; a power source voltage generating unit for supplying a power source voltage to the data driver and the scan driver; and a power source voltage control circuit for stopping a supply of the power source voltage from the power source voltage generating unit to the data driver during a period of the second display" have been fully considered, searched and are persuasive as they do overcome prior art rejection.

3. Claims 1,3-10 and 12-27 are allowed.

4. The following is an examiner's statement of reasons for allowance:

Applicant's argument filed on 02-22-2006, are convincing. As argued by applicant in remarks under claim rejection page 10, last paragraph and page 12, first paragraph and last paragraph, and page 12 the last two paragraphs the prior art of Ishii et al. (6,636,194) in view of

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Yamazaki (6,522,319) and Sato et al. (5,712,652) fails to recite or disclose the uniquely distinct features represented by underlined bold claim below;

A display device comprising: a memory device-built-in pixel portion including a plurality of data lines and a plurality of scan lines arranged in a matrix, a plurality of pixels disposed on respective intersections of the both lines, a plurality of pixel switching elements electrically conducting the data lines and the pixels based on scan signals supplied to the scan lines to write graphic data supplied to the data lines into the pixels, and a plurality of memory devices storing the graphic data supplied to the data lines and being constituted to be capable of supplying the graphic data stored to the pixels corresponding thereto; a data driver and a scan driver for controlling the write of the graphic data supplied to the data lines into the pixels in order to perform a first display; **a memory device driver for controlling the write of the graphic data held in the memory devices into the pixels in order to perform a second display; a power source voltage generating unit for supplying a power source voltage to the data driver and the scan driver; and a power source voltage control circuit for stopping a supply of the power source voltage from the power source voltage generating unit to the data driver during a period of the second display.**

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance.

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***Conclusion***

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prabodh M Dharia whose telephone number is 571-272-7668.

The examiner can normally be reached on M-F 8AM to 5PM.

6. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

7. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Any response to this action should be mailed to:


Commissioner of Patents and Trademarks

Washington, D.C. 20231

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March 25, 2006

  
BIPIN SHALWALA  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600